

FIG. 1 is a block diagram of a system.

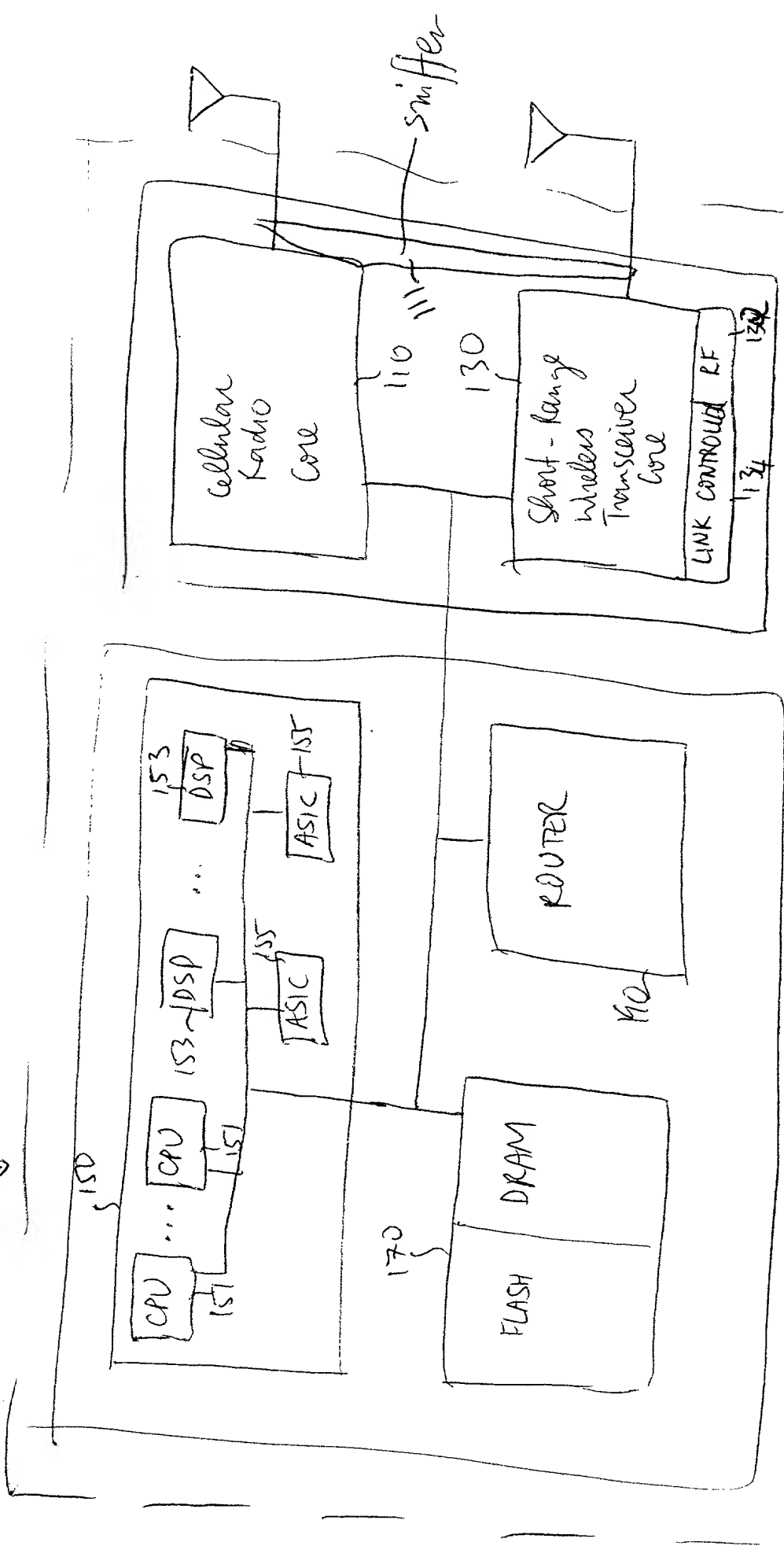


FIG. 1

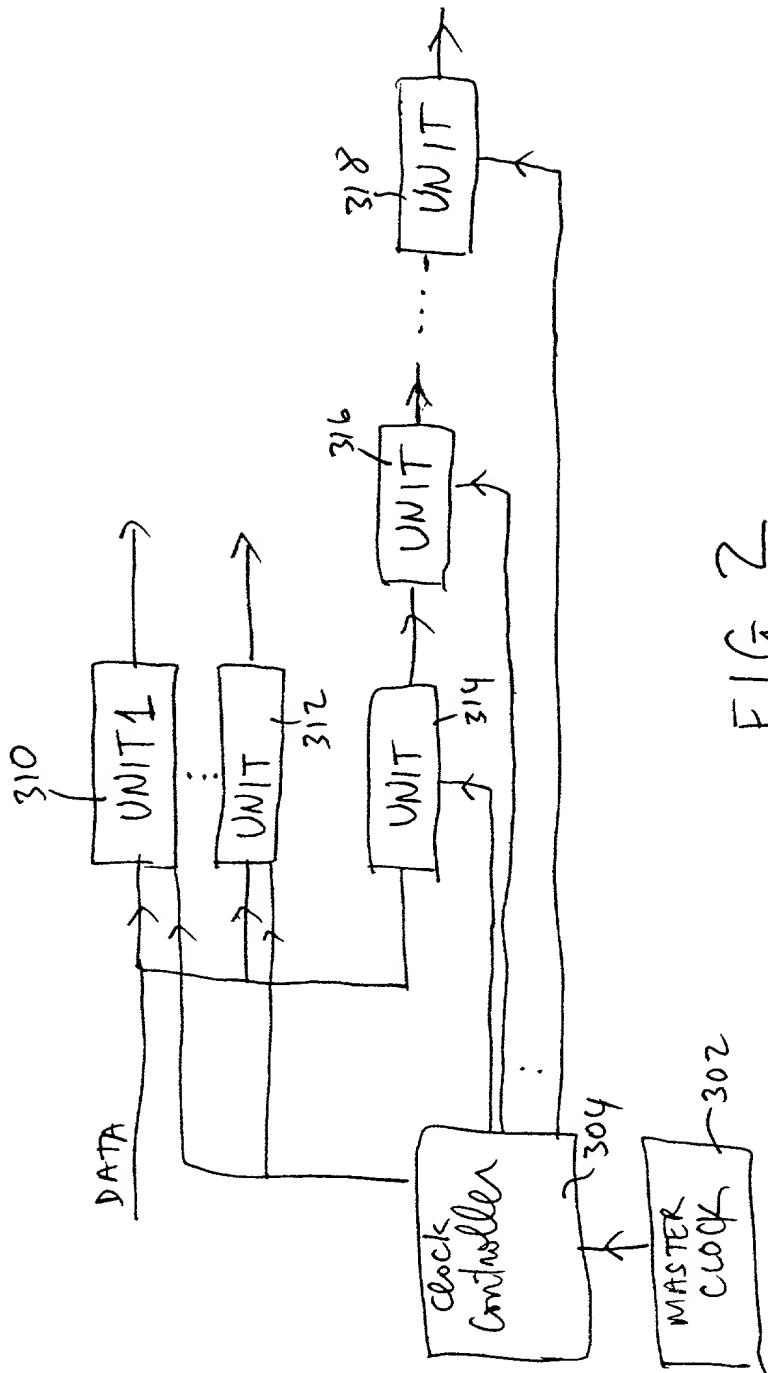


FIG. 2

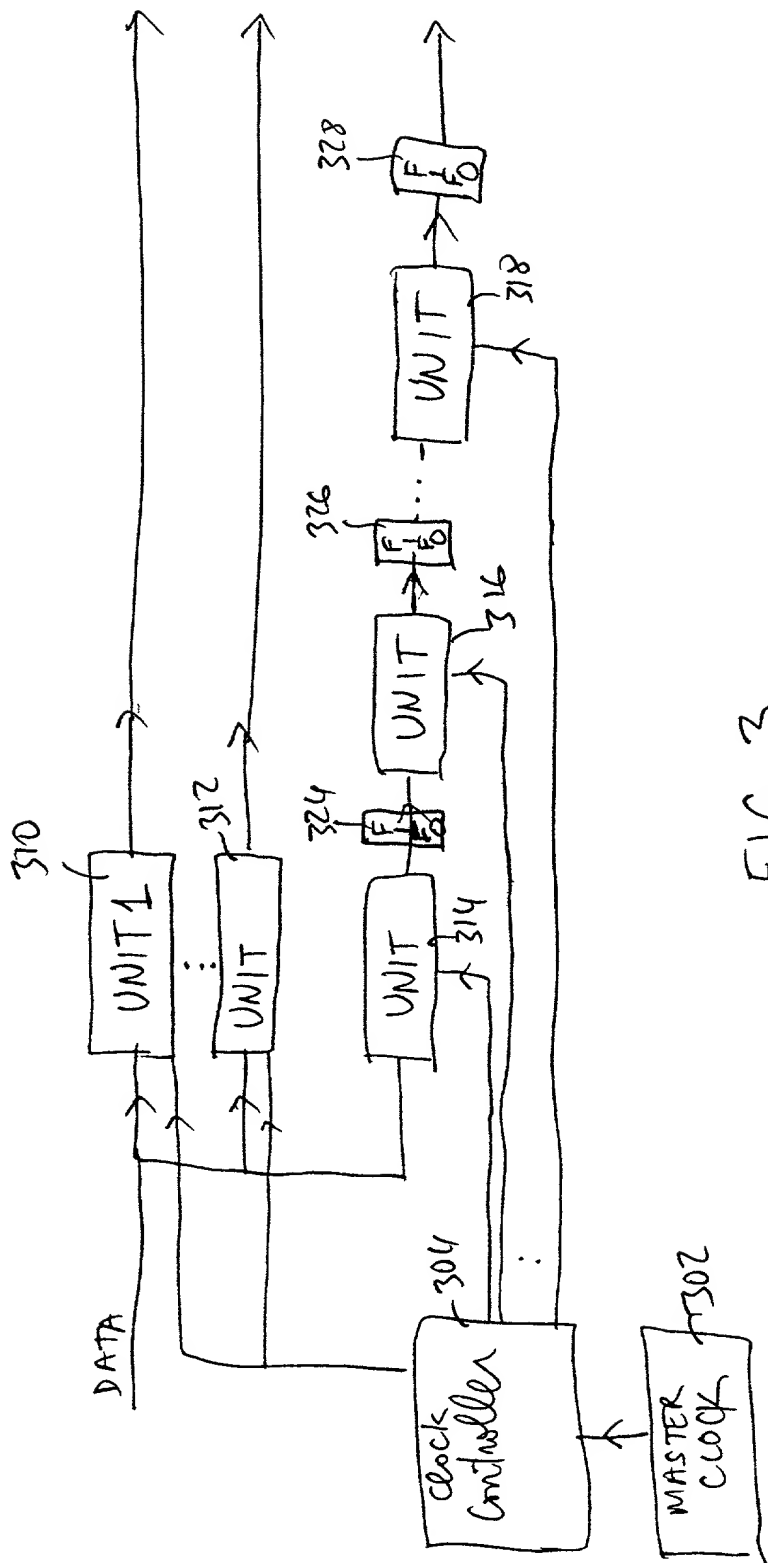


FIG. 3

# MASTER VOLTAGE

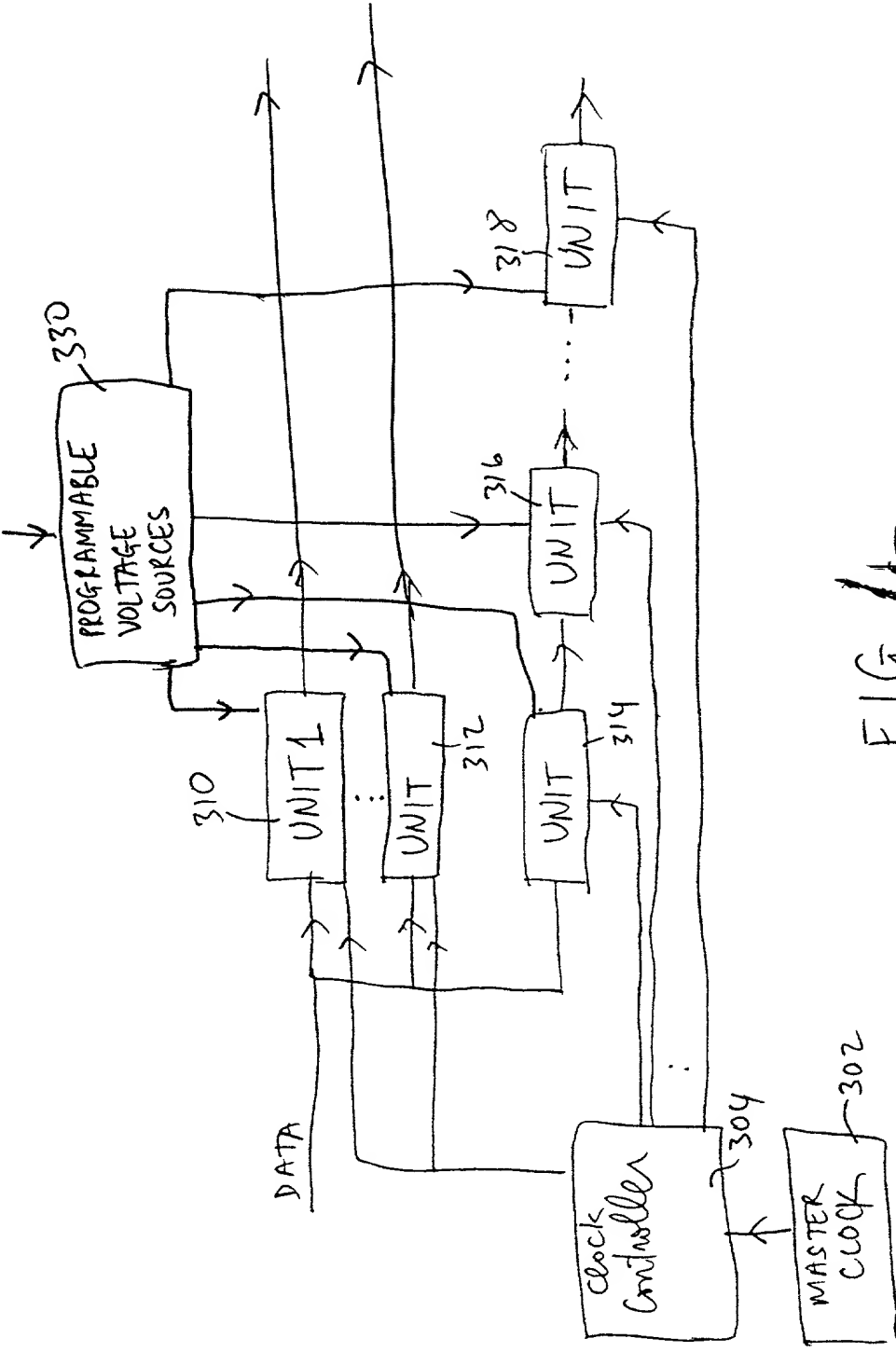
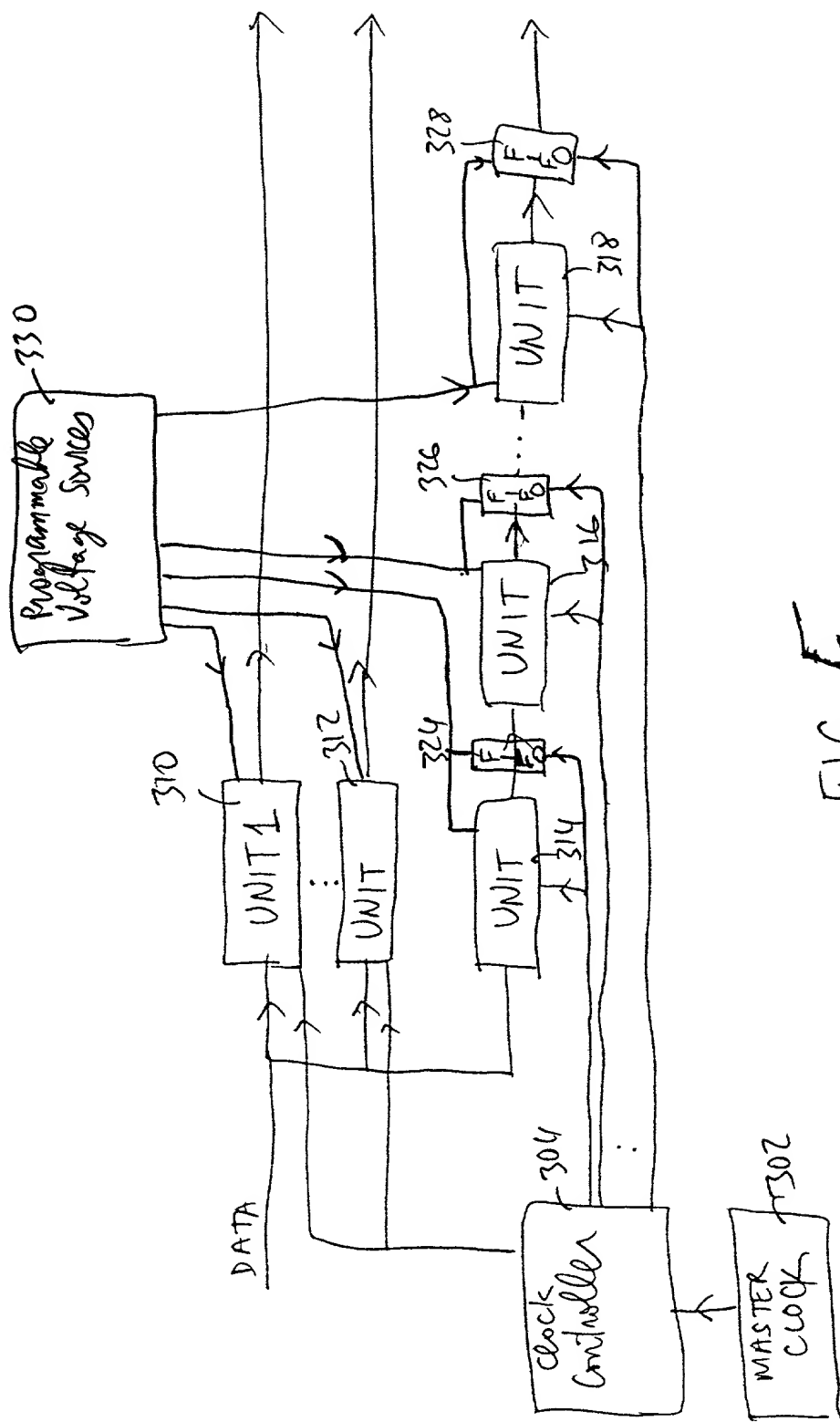


FIG. 4



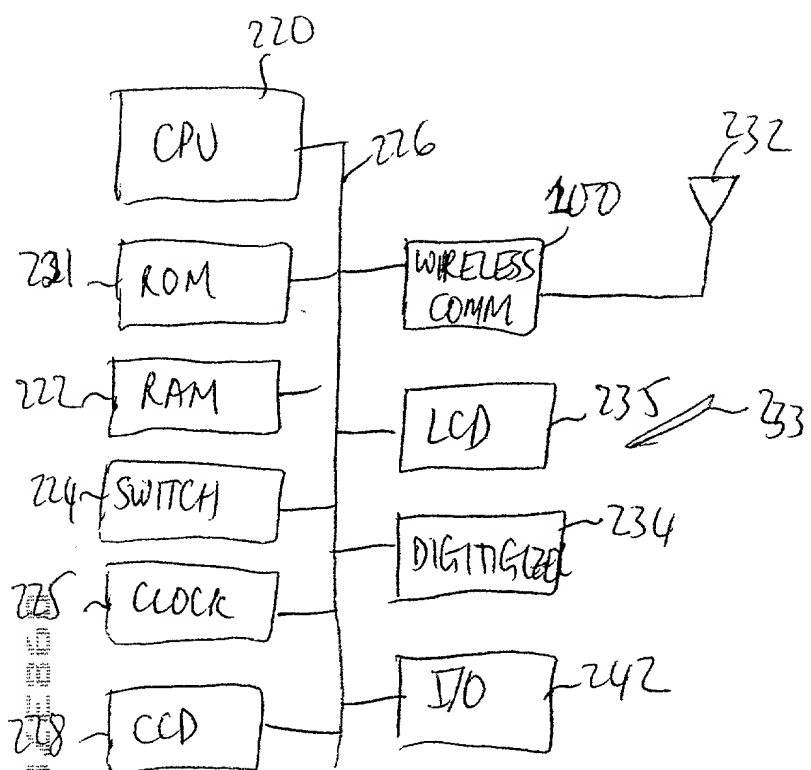


FIG. 2